



IFW

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: June 22, 2004

Signature:   
(Andrew T. Zidel)

Docket No.: SCEI 3.0-200  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Keisuke Inoue

Application No.: 10/812,155

Group Art Unit: 2115

Filed: March 29, 2004

Examiner: Not Yet Assigned

For: METHODS AND APPARATUS FOR  
ACHIEVING THERMAL MANAGEMENT  
USING PROCESSOR MANIPULATION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 C.F.R. §1.97(b)**

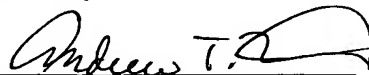
Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. In accordance with the notice at 1276 O.G. 55, copies of U.S. patent and patent application references are not enclosed herewith; copies of non-U.S. patent references are submitted herewith. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: June 22, 2004

Respectfully submitted,

By   
Andrew T. Zidel

Registration No.: 45,256  
LERNER, DAVID, LITTENBERG, KRUMHOLZ  
& MENTLIK, LLP  
600 South Avenue West  
Westfield, New Jersey 07090  
(908) 654-5000  
Attorney for Applicant

LD-458\



PTO/SB/08a/b (08-03)  
Approved for use through 07/31/2006. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

<b>Substitute for form 1449A/B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/812,155
				Filing Date	3/29/2004
				First Named Inventor	Keisuke Inoue
				Art Unit	2115
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-200

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	AA	US-5,274,797	12/28/1993	Barlow et al.	
	AB	US-5,715,184	02/03/1998	Tyler et al.	
	AC	US-5,761,516	06/02/1998	Rostoker et al.	
	AD	US-2002/0053684 A1	05/09/2002	Chauvel et al.	
	AE	US-2002/0065049 A1	05/30/2002	Chauvel et al.	
	AF	US-2003/0125900 A1	07/03/2003	Orenstien et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
	CA	Azevedo et al., "Architectural and Compiler Strategies for Dynamic Power Management in the COPPER Project," 2001 International Workshop on Innovative Architecture, January 2001.			
	CB	Heo et al., "Reducing Power Density through Activity Migration," Proceedings of the 2003 International Symposium on Low Power Electronics and Design (ACM Press, ©2003), pp. 217-222.			
	CC	Tiwari et al., "Instruction Level Power Analysis and Optimization of Software," Journal of VLSI Signal Processing, Vol. 13, No. 2 (August 1996), pp. 1-18.			
	CD	Abhishek Mehta, "Techniques for Low Power Processing - A Survey" CSE 822, Spring 2001, A29838087, Retrieved from the Internet on March 29, 2004, <URL: <a href="http://www.cs.vcr.edu/~chzhang/papers_read/lowpower_survey.pdf">www.cs.vcr.edu/~chzhang/papers_read/lowpower_survey.pdf</a> >; 10 pages.			
	CE	Lee et al., "Power Analysis and Minimization Techniques for Embedded DSP Software," IEEE Transactions on VLSI Systems, Vol. 5, No. 1, March 1997, pp. 123-135 (manuscript copy submitted).			
	CF	Gerosa et al., "A 250-MHz 5-W PowerPC Microprocessor with On-Chip L2 Cache Controller," IEEE Journal of Solid-State Circuits, Vol. 32, No. 11, November 1997, pp. 1635-1649.			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--